

10/085023
03/01/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10085023	03/01/2002	257		2841	

****APPLICANTS:** Kurihara Hideo; Iijima Mitsuteru; Itano Kiyoshi; Chida Tetsuya;

BEST AVAILABLE COPY

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A CON OF PCT/JP00/03468 05/30/2000

**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 11-250780 09/03/1993

PG-PU <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 020137
35 USC 110 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiner's initials	
TITLE: Semiconductor memory and method of manufacture thereof	

ISSUE FEE Amount Due Date Paid		Assistant Examiner	Total Claims	Print Claim for O.G.	
			DRAWING		
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	Sheets Drwg.	Figs. Drwg.	Print Fig.
			Application Examiner		
		PREPARED FOR ISSUE	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)